

FIG. 1
PRIOR ART

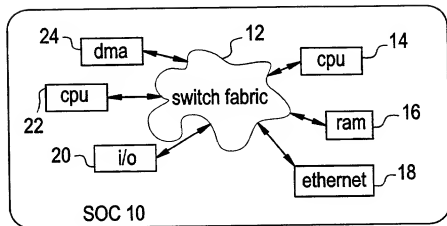


FIG. 2

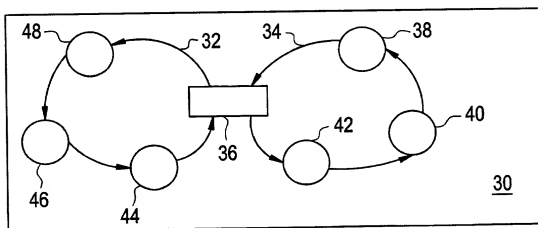


FIG. 3

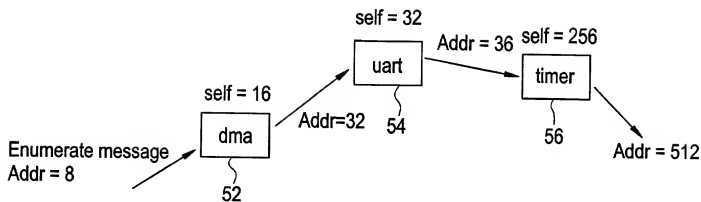


FIG. 4

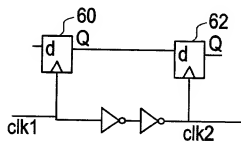


FIG. 5

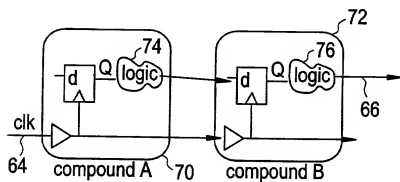


FIG. 6

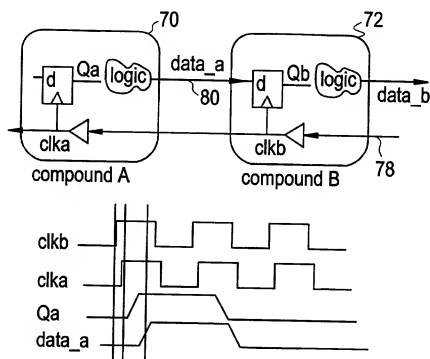


FIG. 7

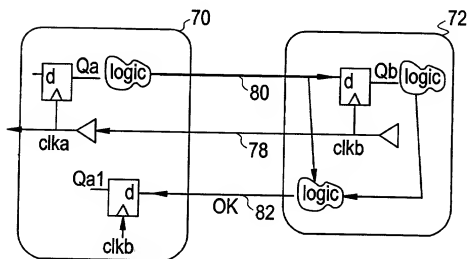


FIG. 8

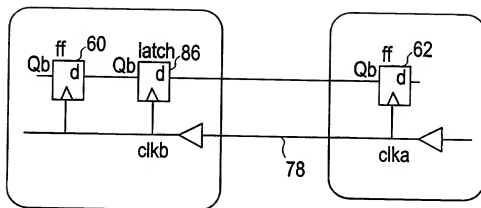


FIG. 9

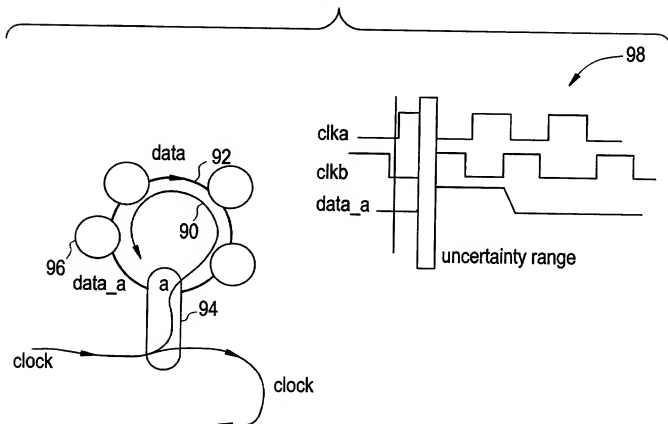


FIG. 10

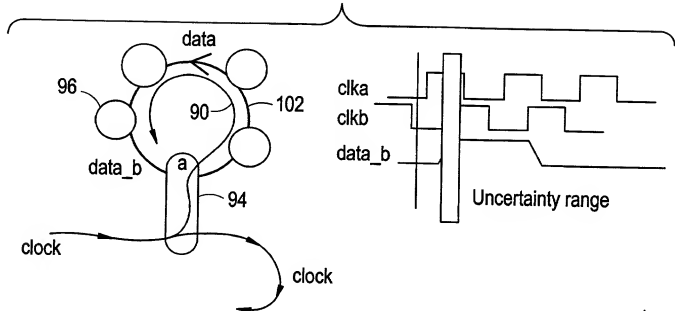


FIG. 11

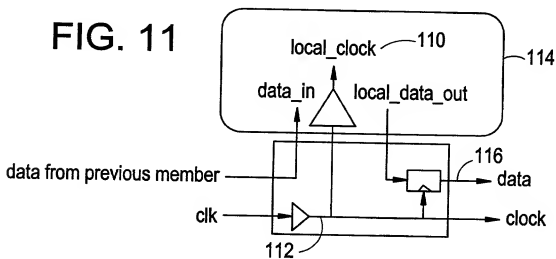


FIG. 12

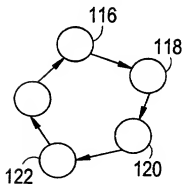


FIG. 13

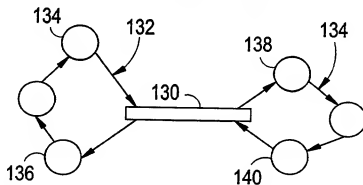


FIG. 14

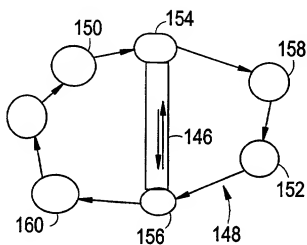


FIG. 15

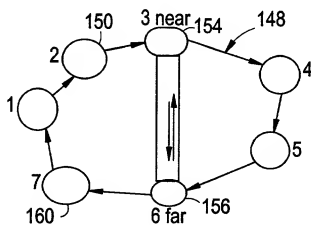


FIG. 16

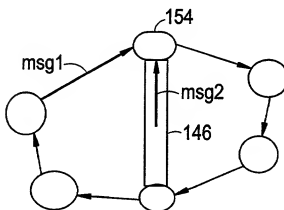


FIG. 17

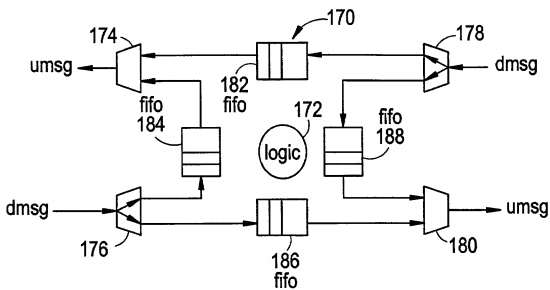


FIG. 18

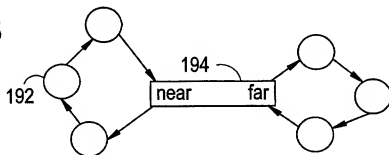


FIG. 19

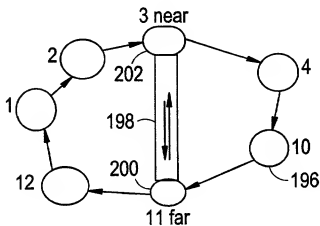


FIG. 20

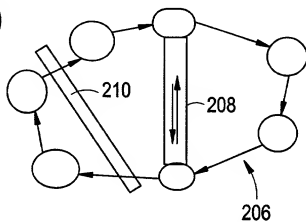


FIG. 21

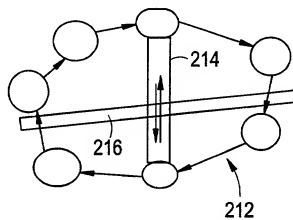
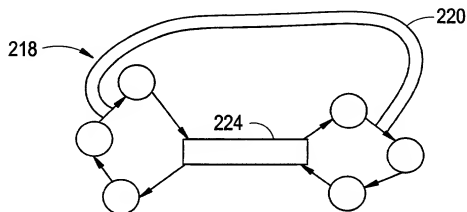


FIG. 22



9/64

FIG. 23

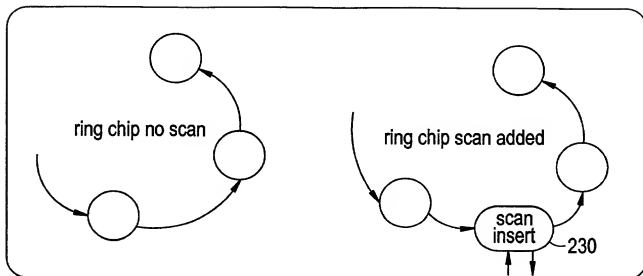


FIG. 24

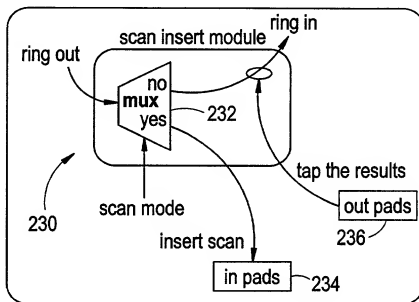
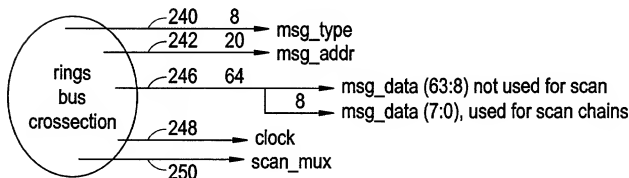


FIG. 25



2024240" 2141410012

FIG. 26

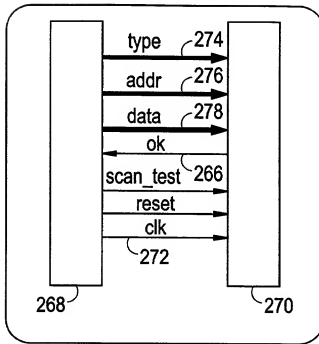


FIG. 27

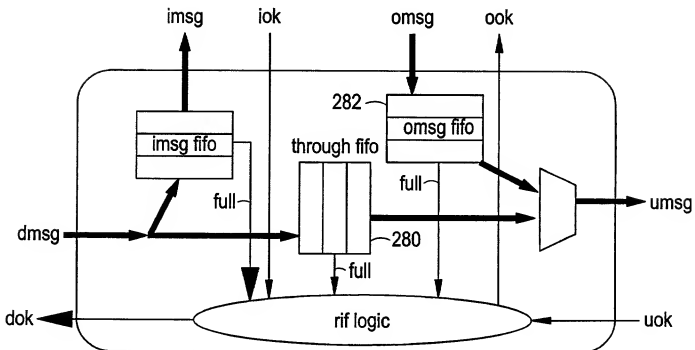


FIG. 28

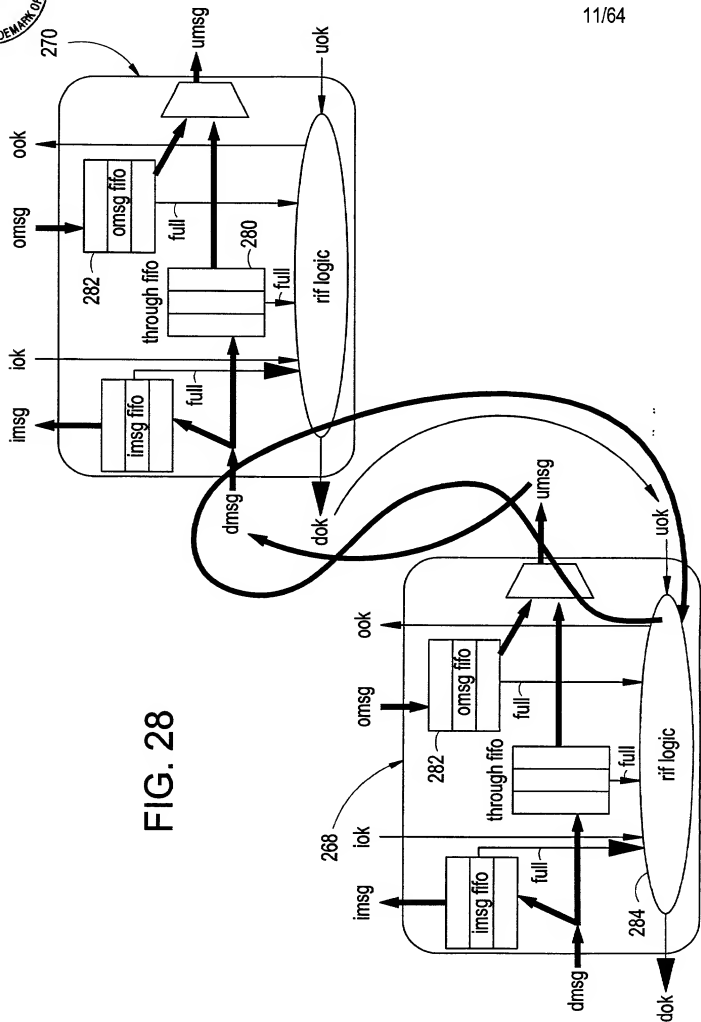


FIG. 29

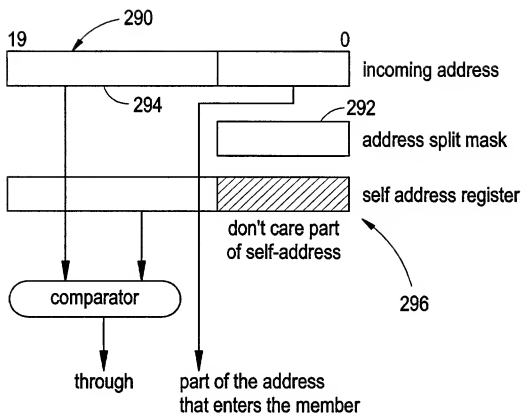


FIG. 30

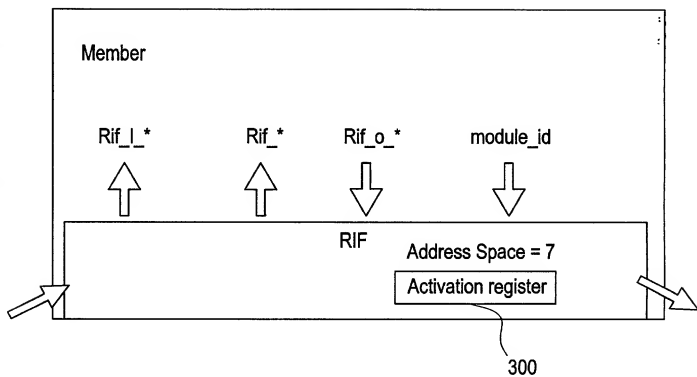


FIG. 31

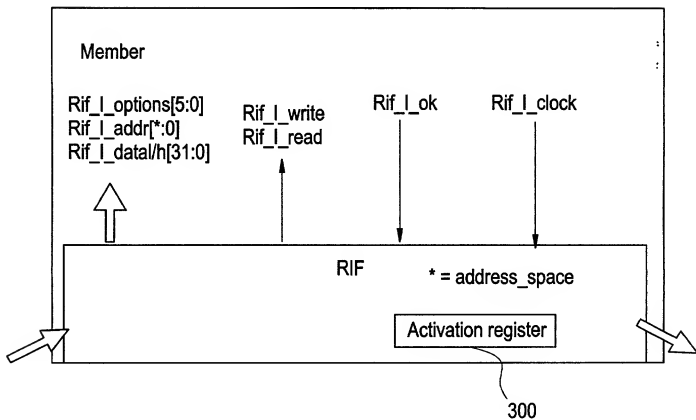
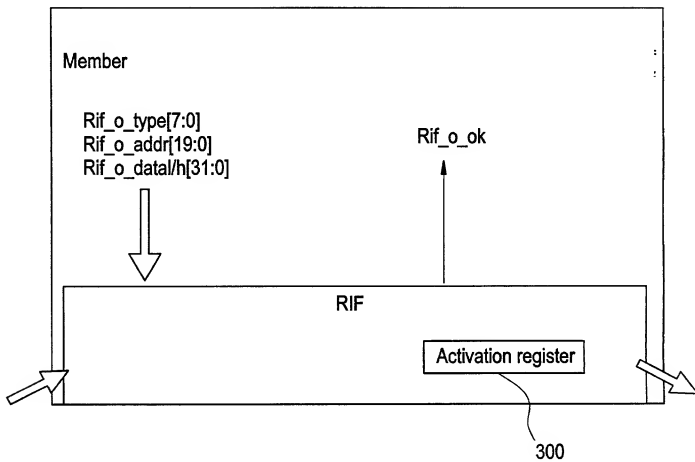


FIG. 32



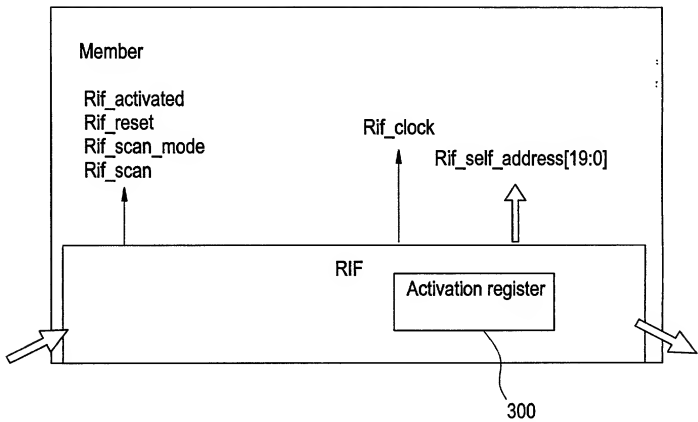
[illegible]

FIG. 34

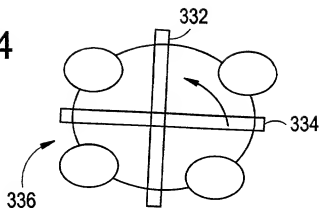


FIG. 35

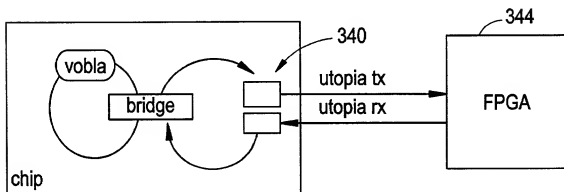


FIG. 36

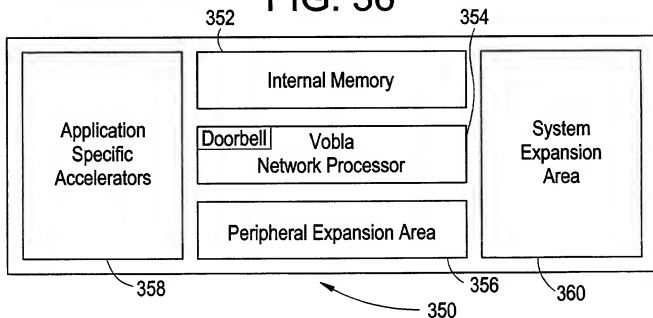
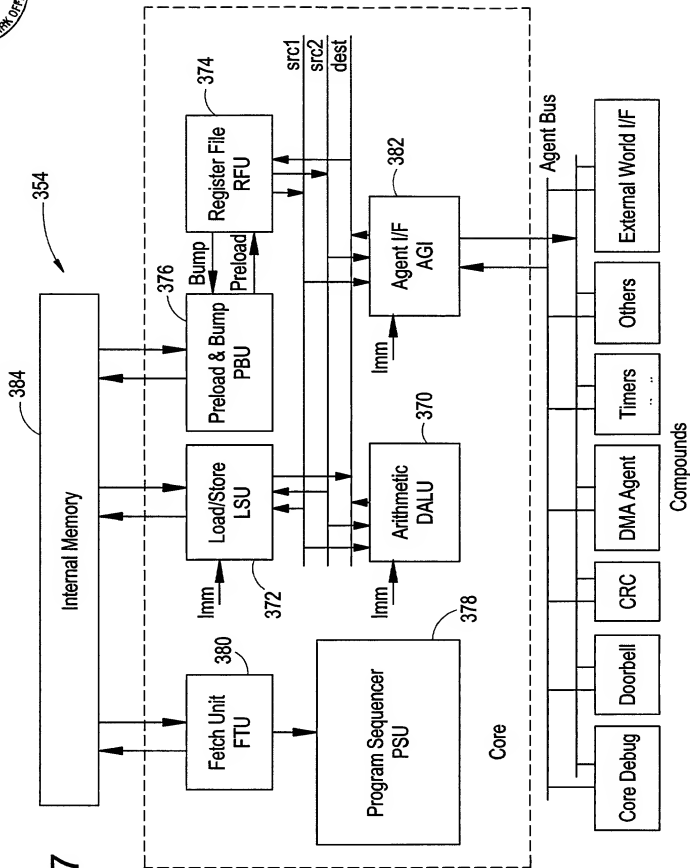


FIG. 37

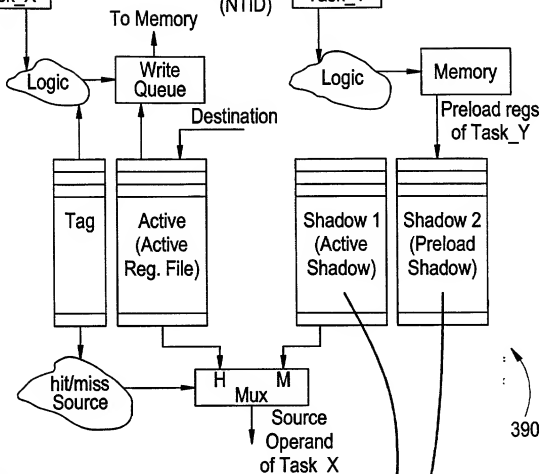


Current Task (CTID)

Task_X

Next Task (NTID)

Task_Y



After a task switch

Current Task (CTID)

Task_Y

Next Task (NTID)

Task_Z

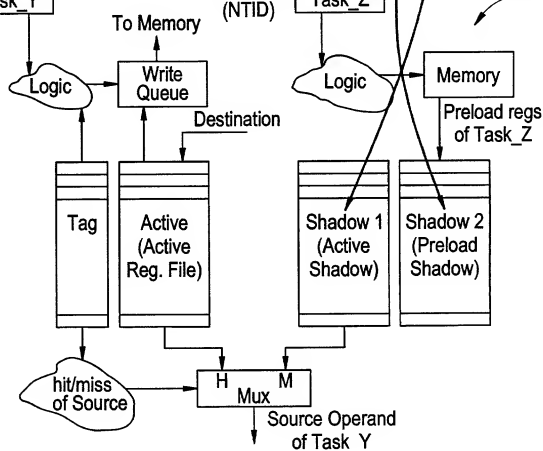


FIG. 39

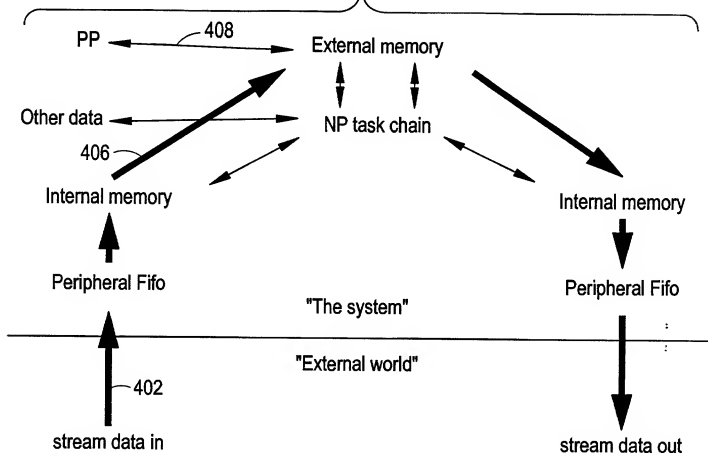


FIG. 40

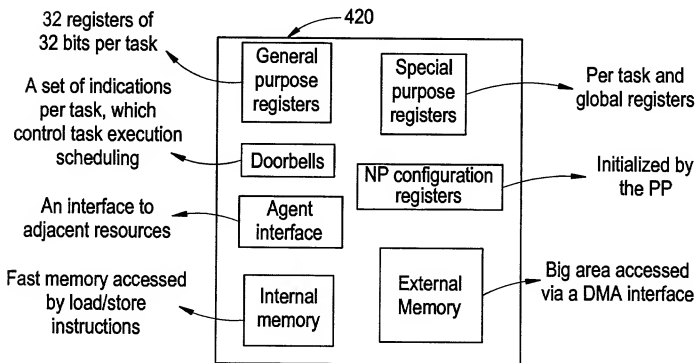
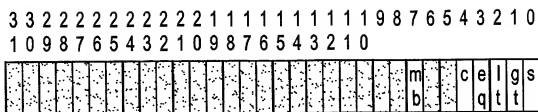


FIG. 41

R1 register



- s - sticky bit
- eq - equal/zero
- lt - less then/negative
- gt - greater then/positive
- c - carry
- mb - reflection of the RAM multi-reader busy indication

FIG. 42

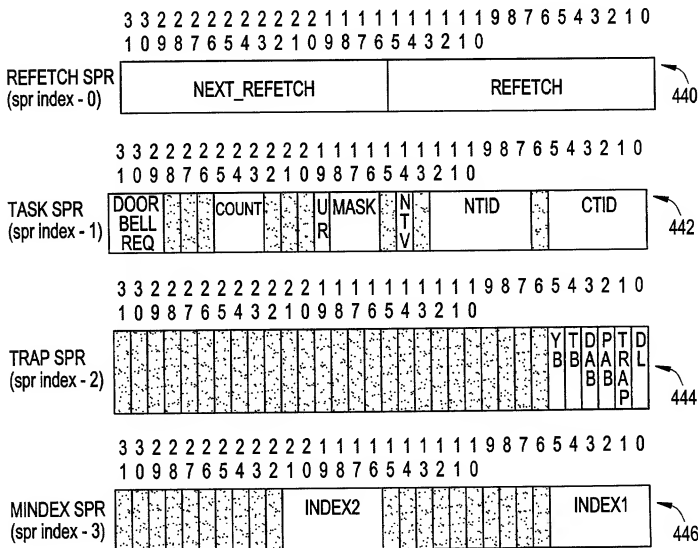


FIG. 43

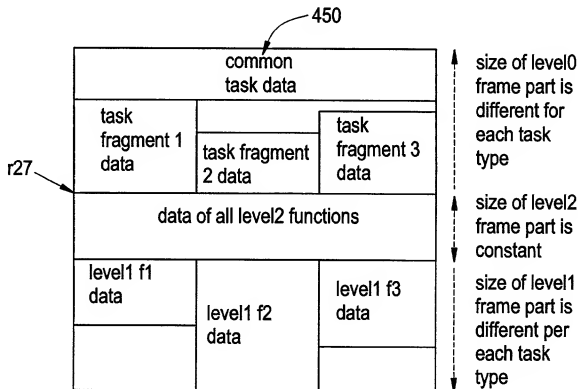


FIG. 44

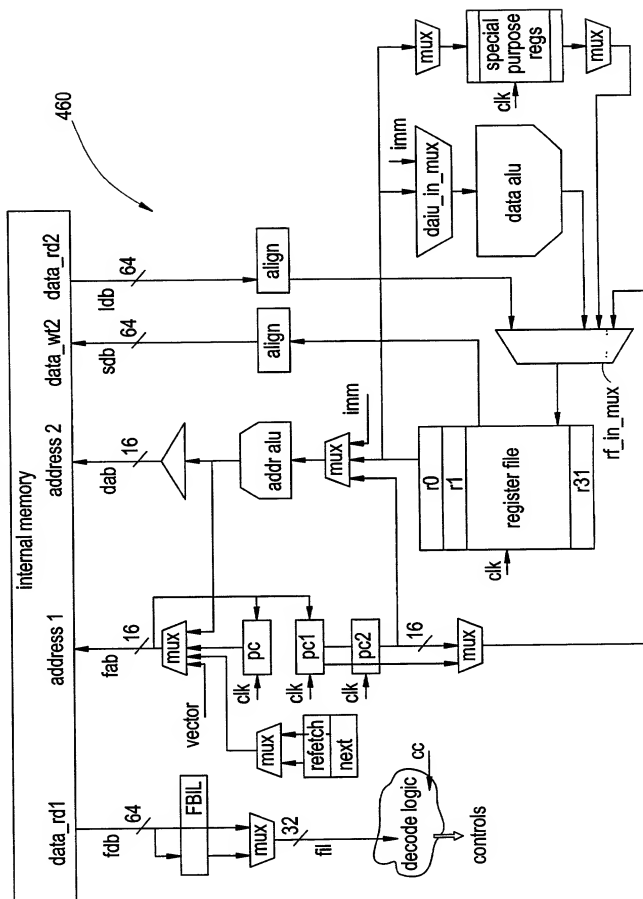
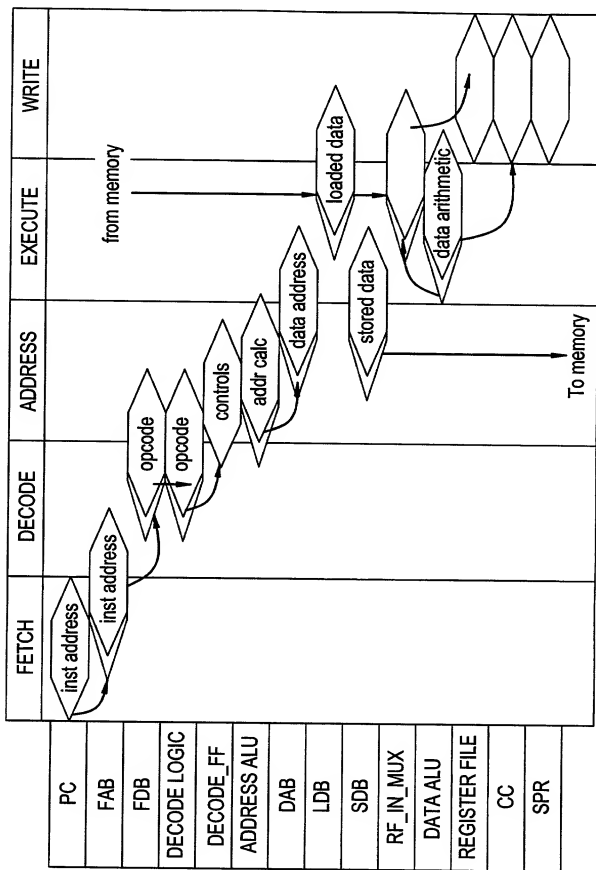


FIG. 45



- Flip Flop
 - Logic

FIG. 46

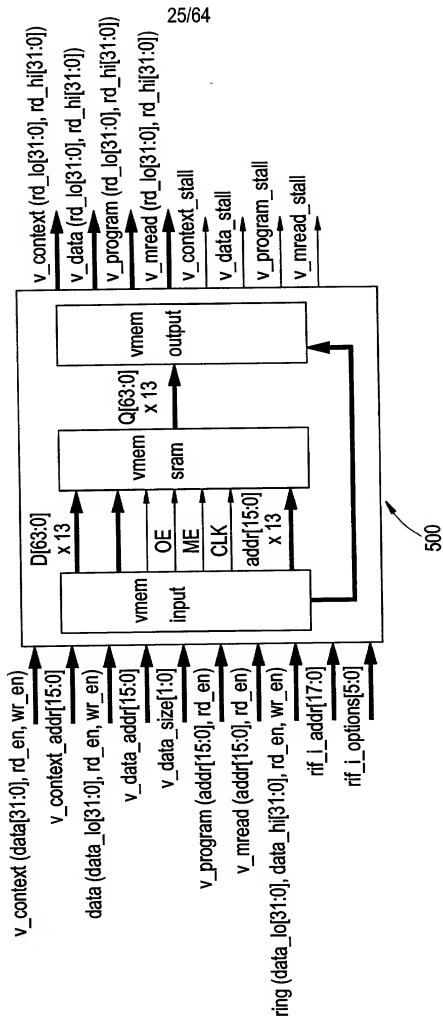


FIG. 47

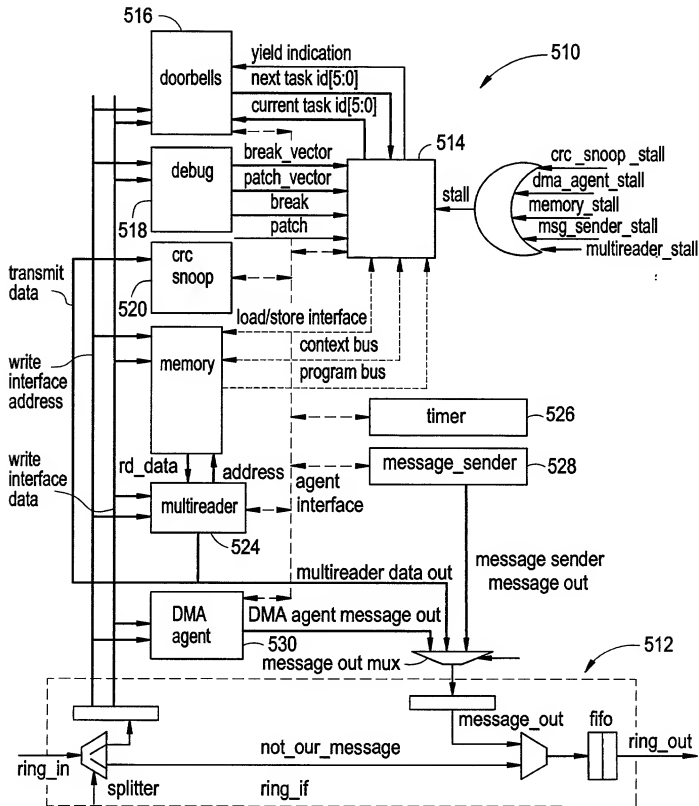


FIG. 48

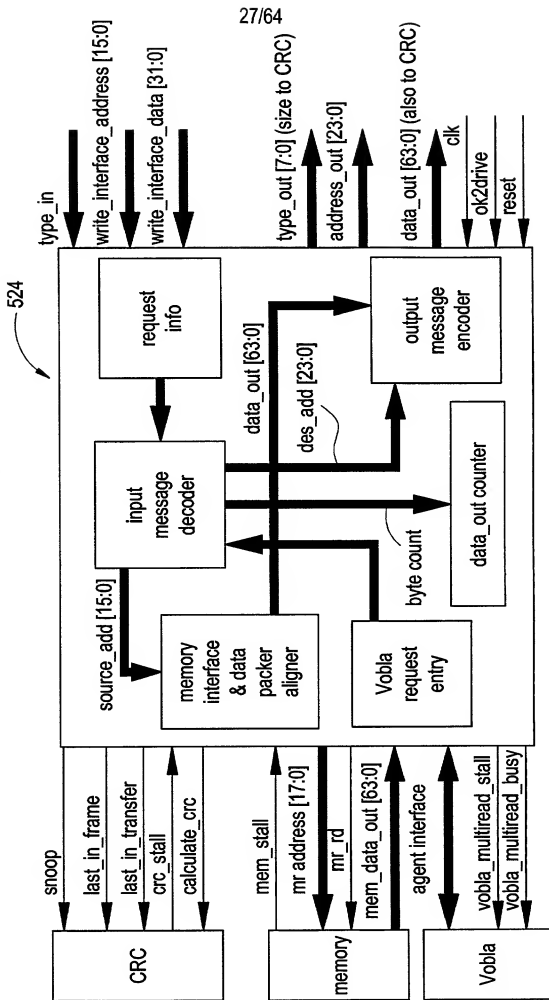


FIG. 49

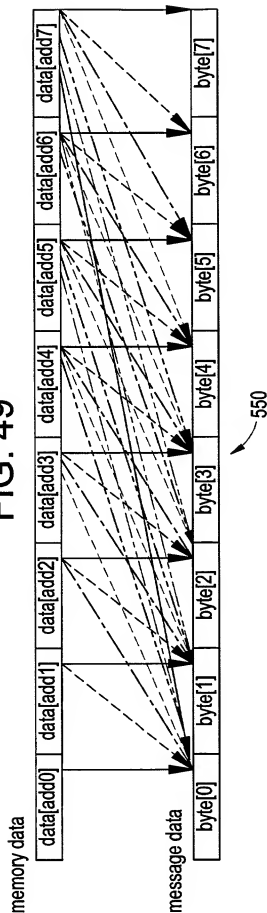


FIG. 50

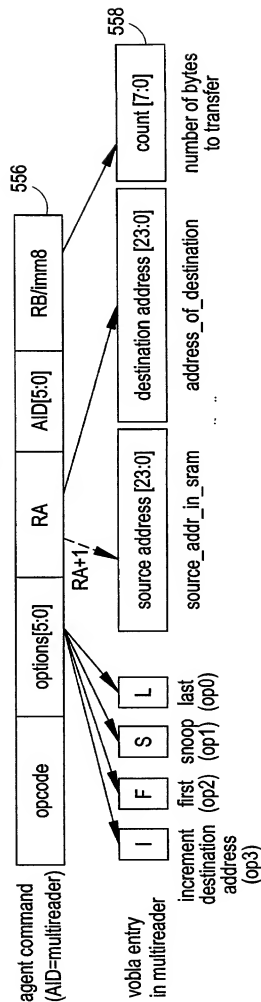


FIG. 51

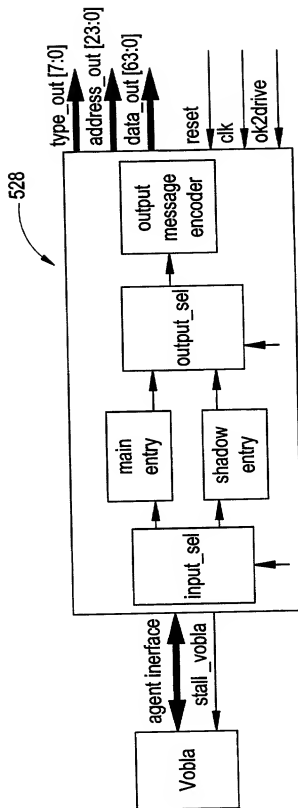


FIG. 52

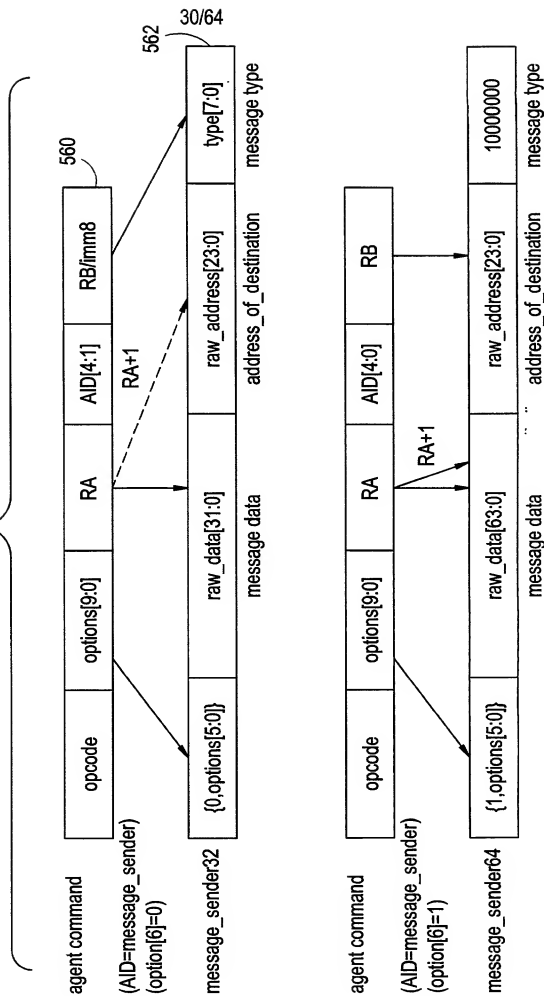


FIG. 53



Figure 1: Detailed description of the format of the agent command. The diagram shows a 32/64-bit agent command structure. The command is divided into several fields: opcode, options[10:0], RA, AID[4:0], RB/imm8, and a 576-bit field. The options field is further divided into M (modify address), U (urgent), D (dir), A (auto), NA (send ack), and L (long). The RA field points to a RAM address [31:0]. The AID field points to a RAM address [23:0]. The RB/imm8 field points to a count [7:0]. The 576-bit field is divided into a RAM address [31:0] and a RAM address [23:0]. The diagram also shows a RAM address [31:0] and a RAM address [23:0] with their respective labels.

FIG. 55

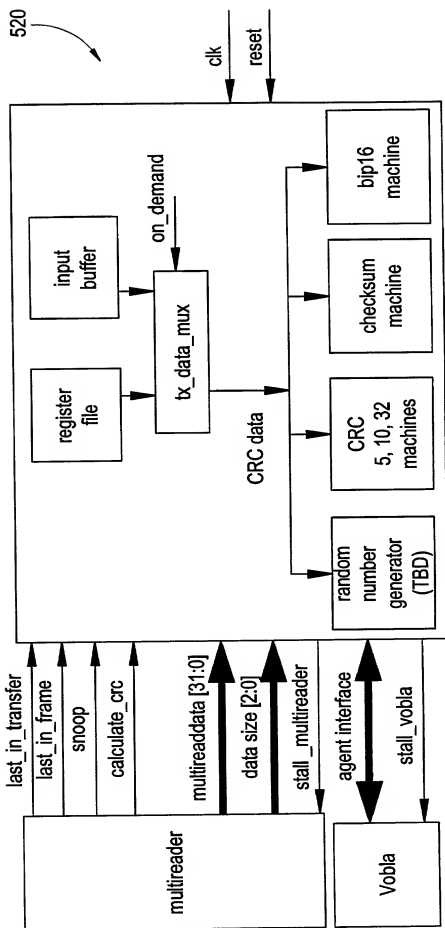


FIG. 56

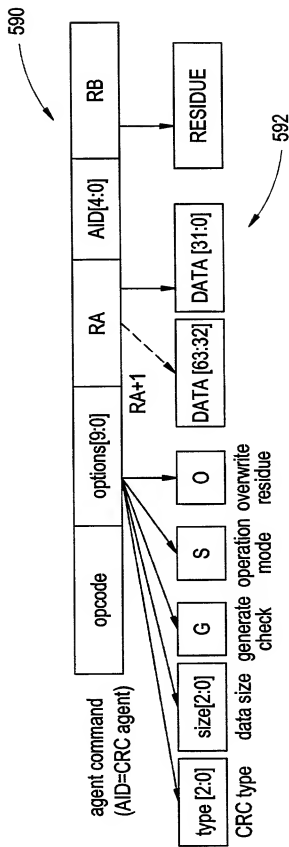


FIG. 57

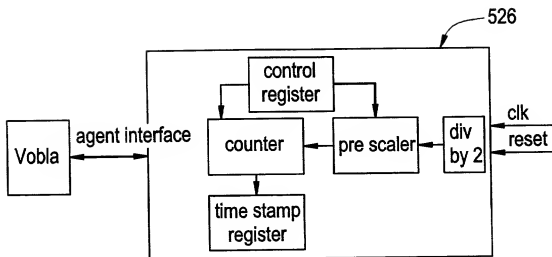


FIG. 58

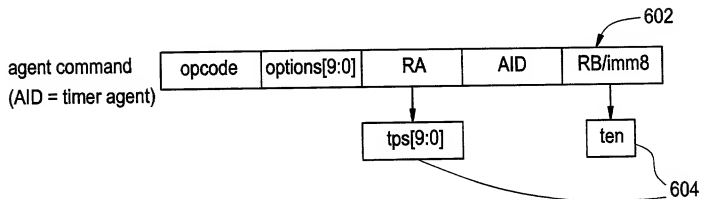


FIG. 59

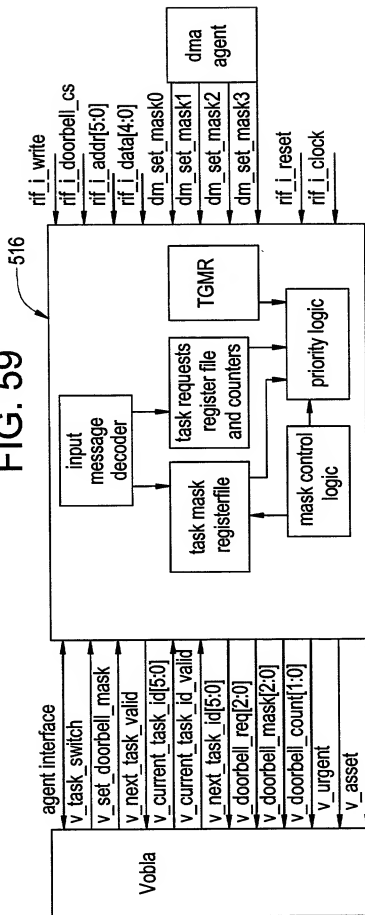


FIG. 60

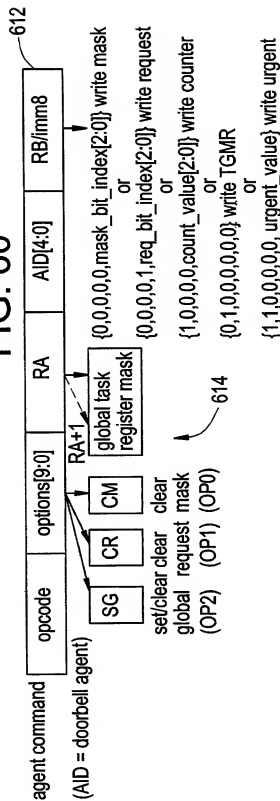


FIG. 61

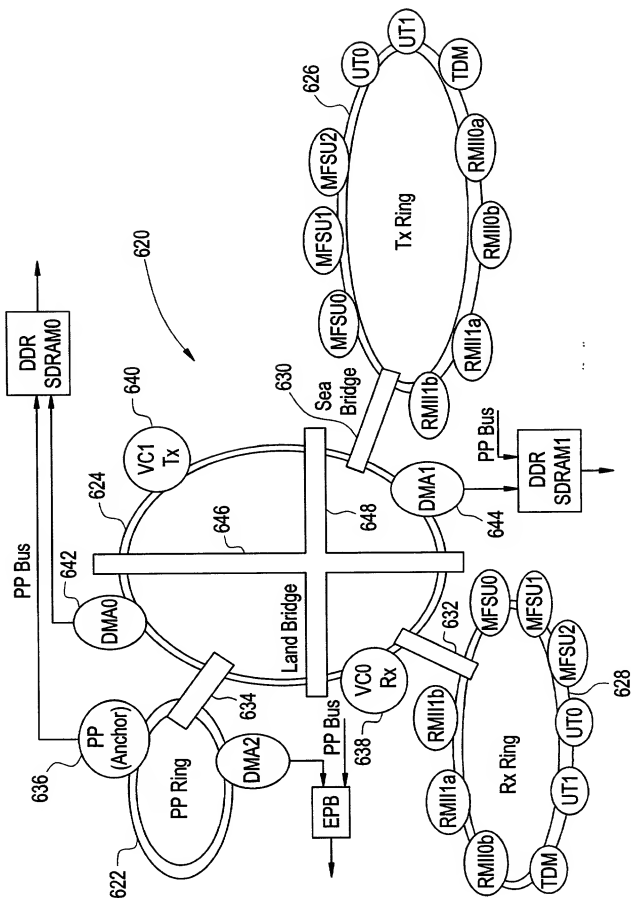


FIG. 62

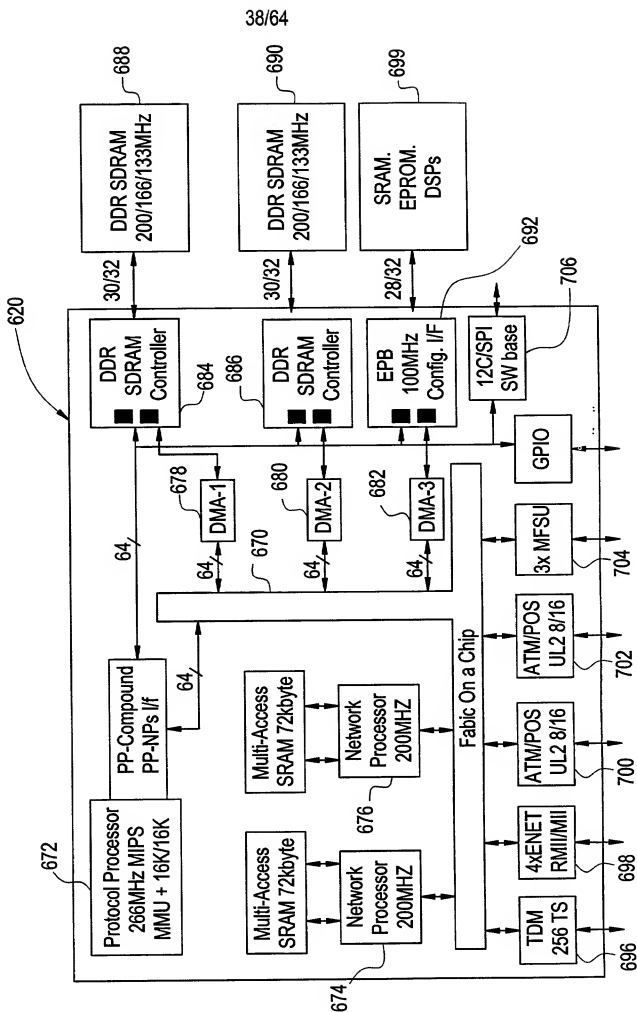


FIG. 63

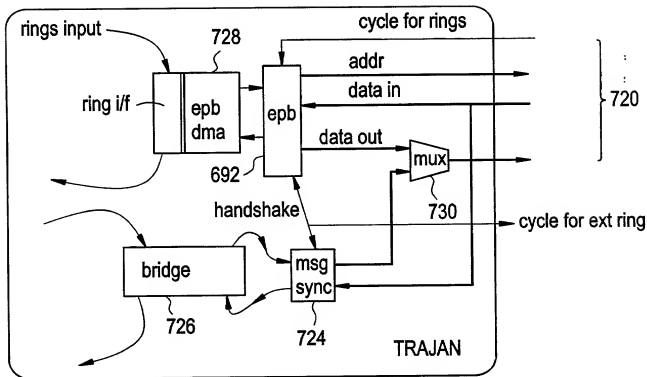


FIG. 64

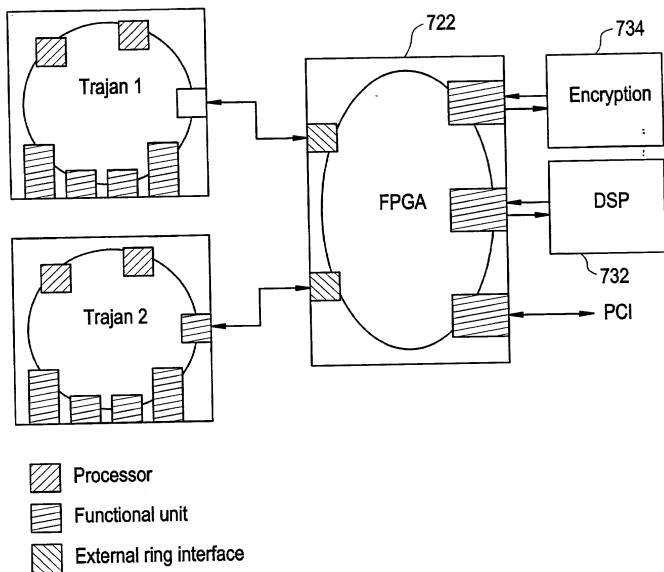


FIG. 65

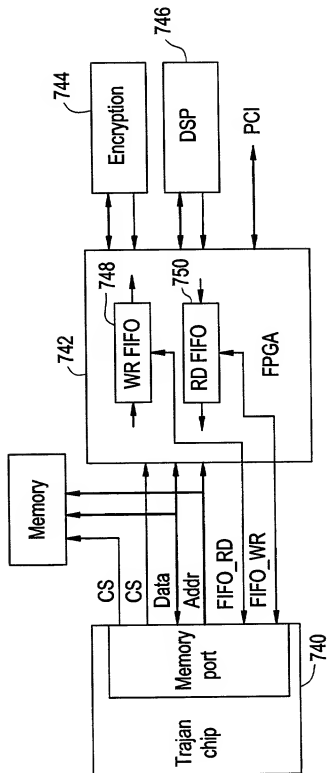


FIG. 66

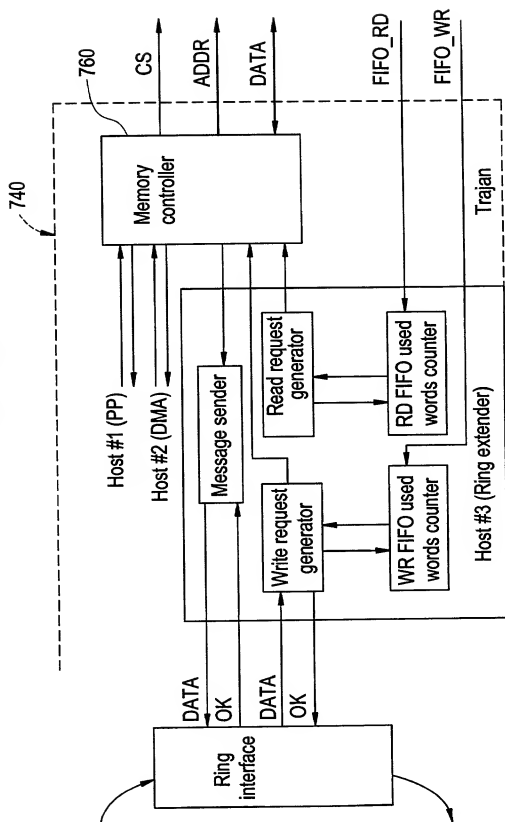


FIG. 67

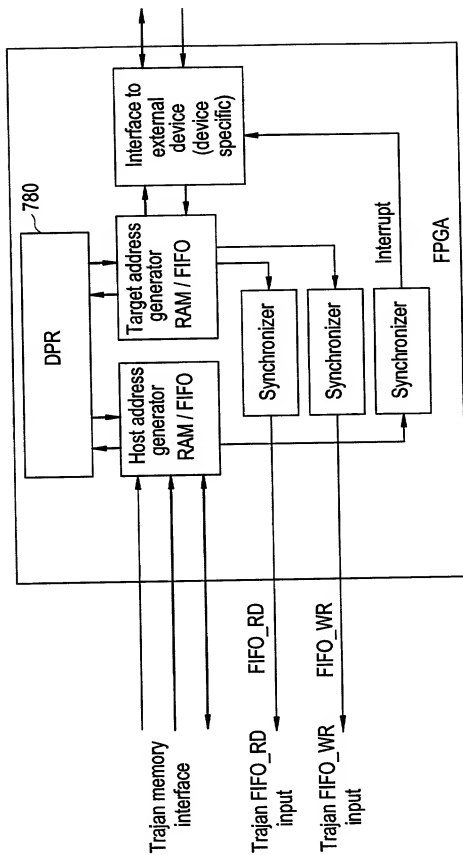


FIG. 68

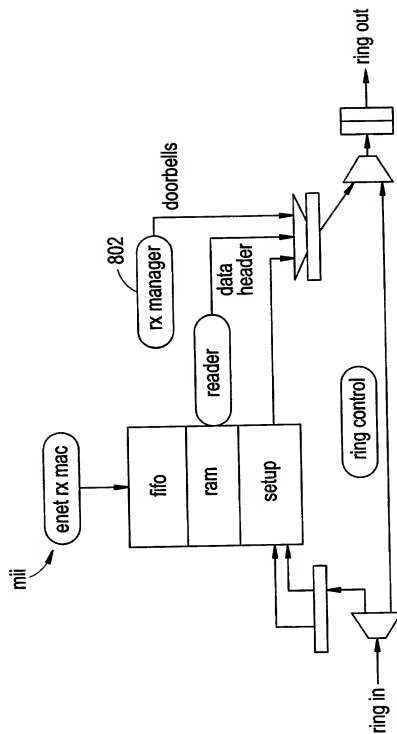


FIG. 69

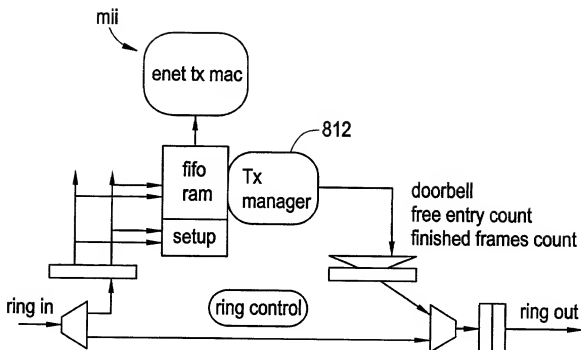
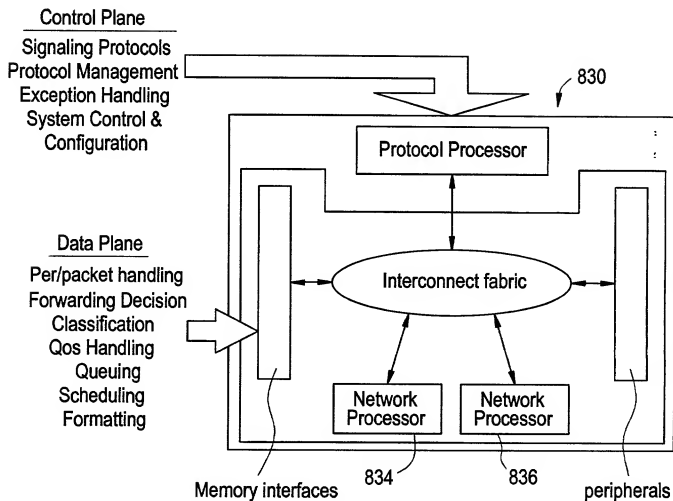


FIG. 70



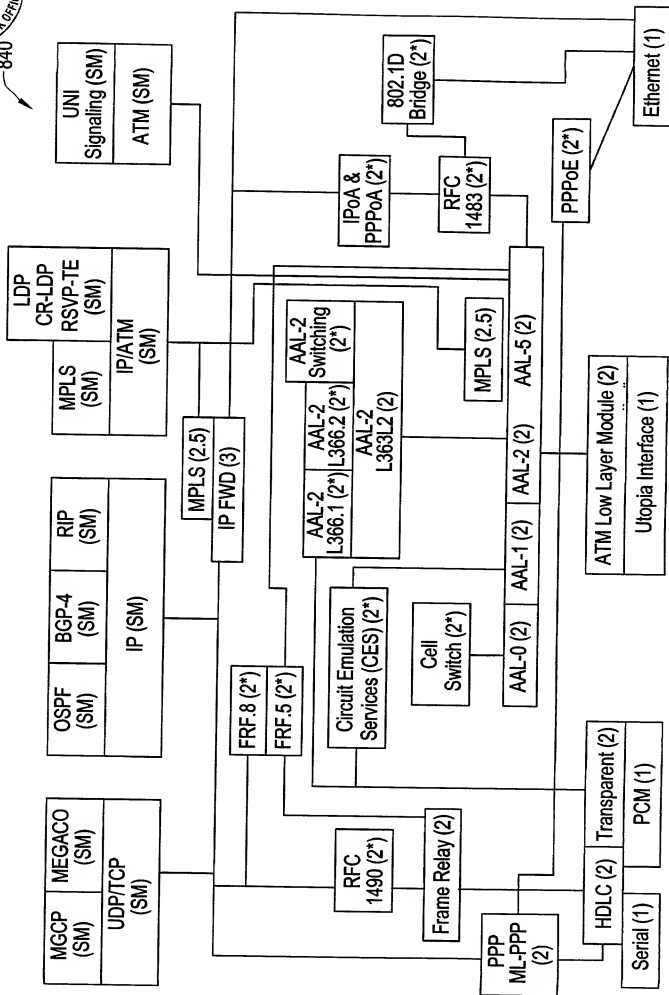


FIG. 72

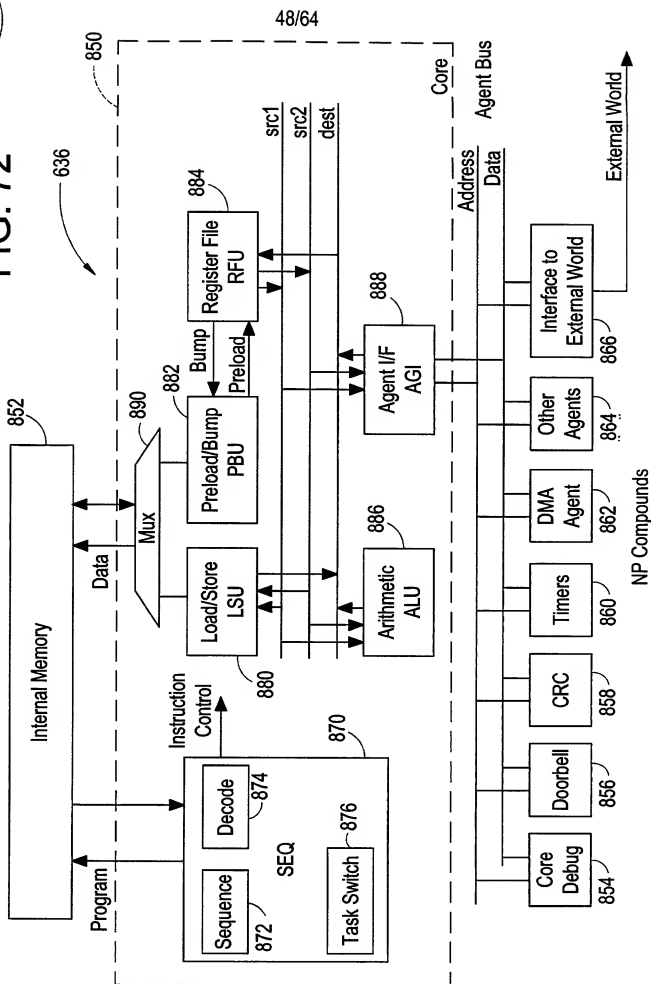


FIG. 74

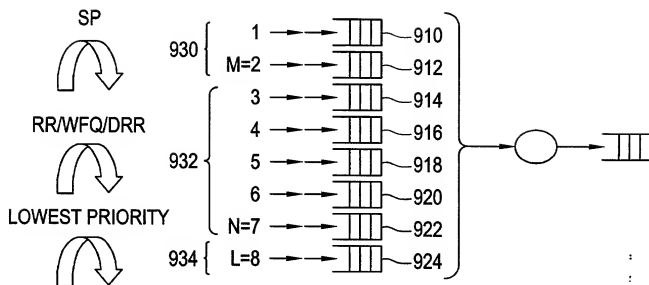


FIG. 75

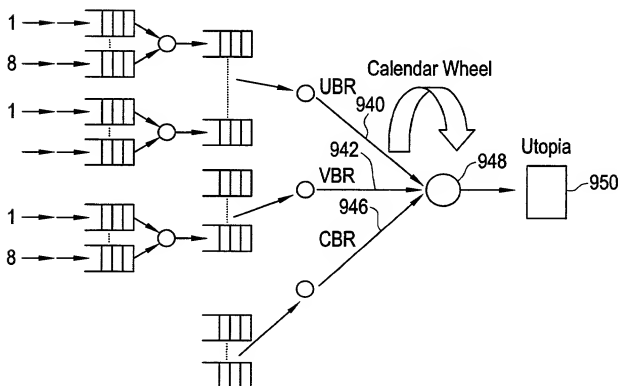


FIG. 76

960

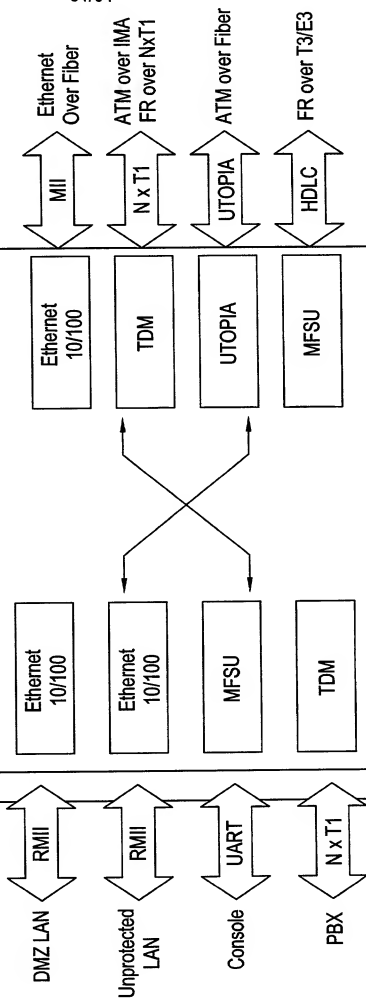


FIG. 77

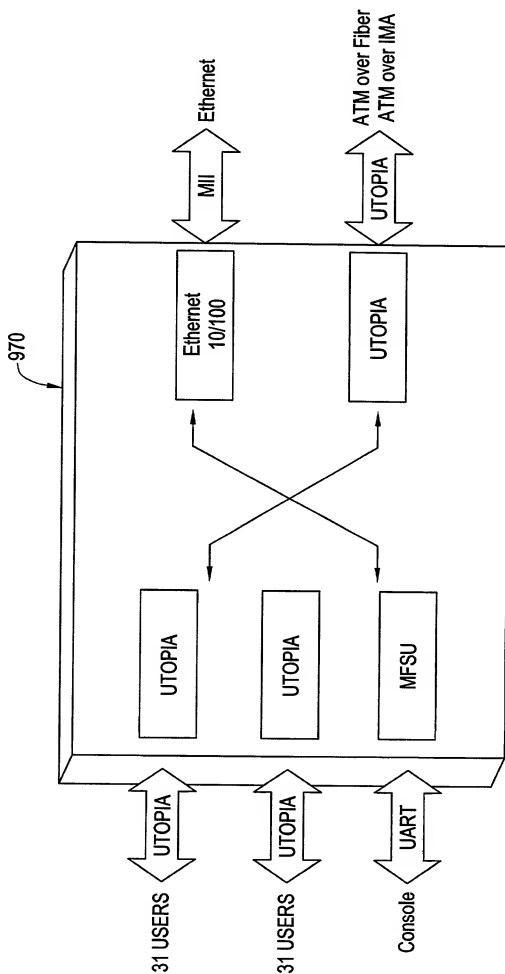


FIG. 78

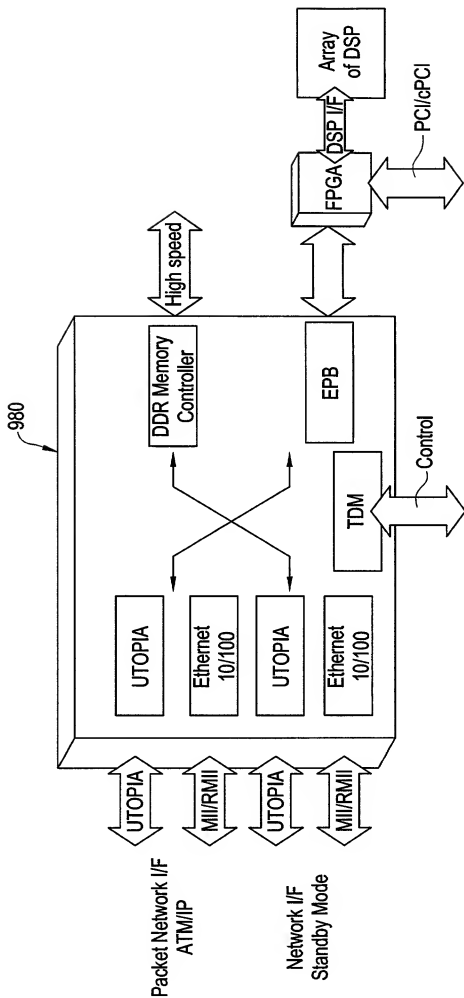


FIG. 79

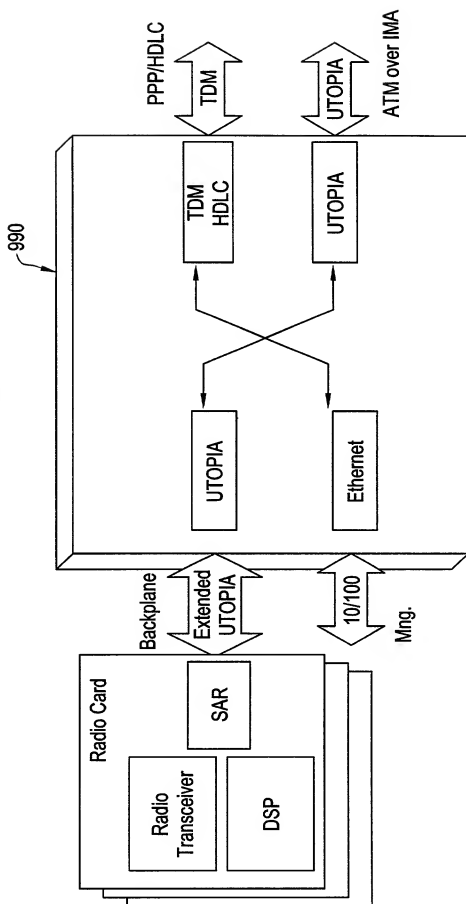


FIG. 80

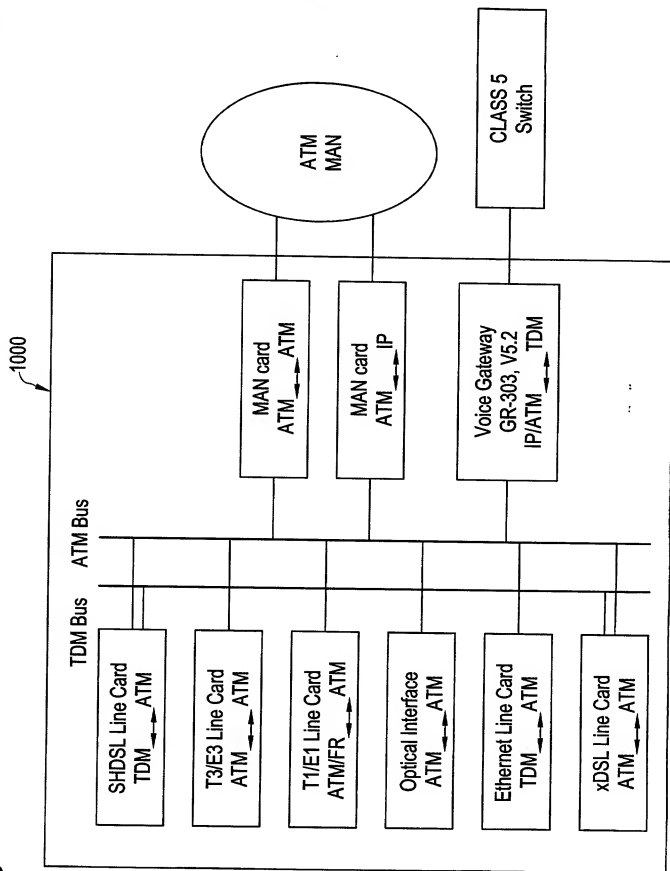


FIG. 81

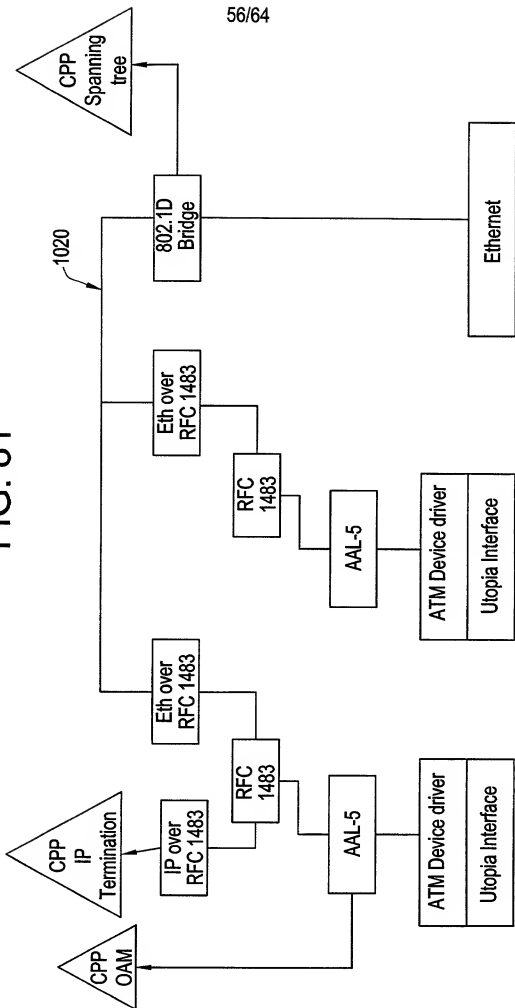


FIG. 83

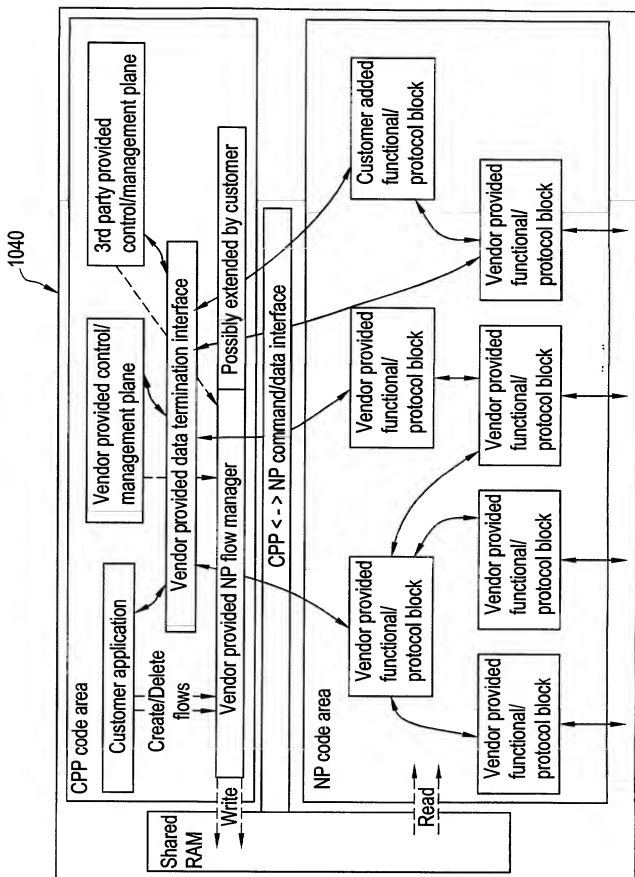


FIG. 84

1050

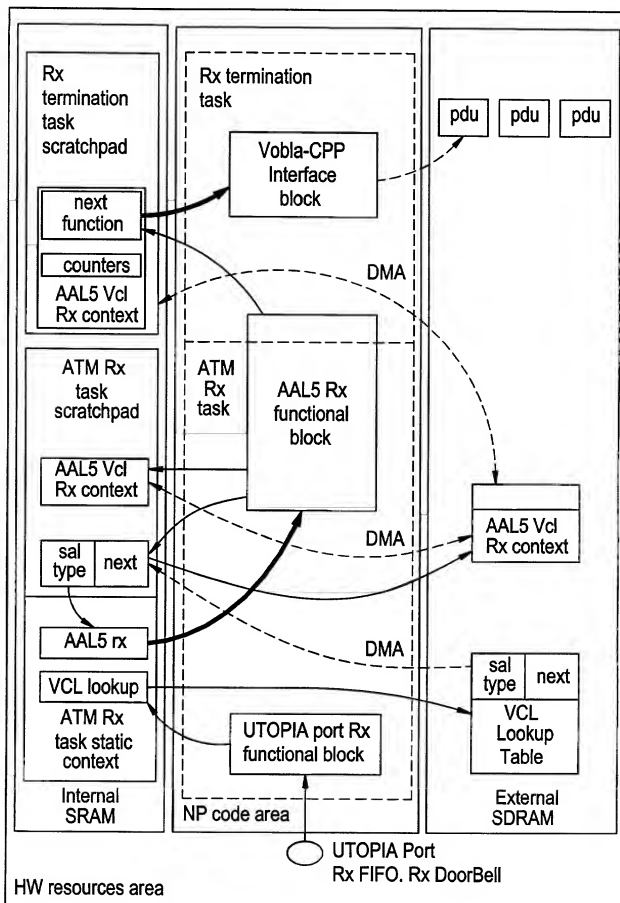


FIG. 85

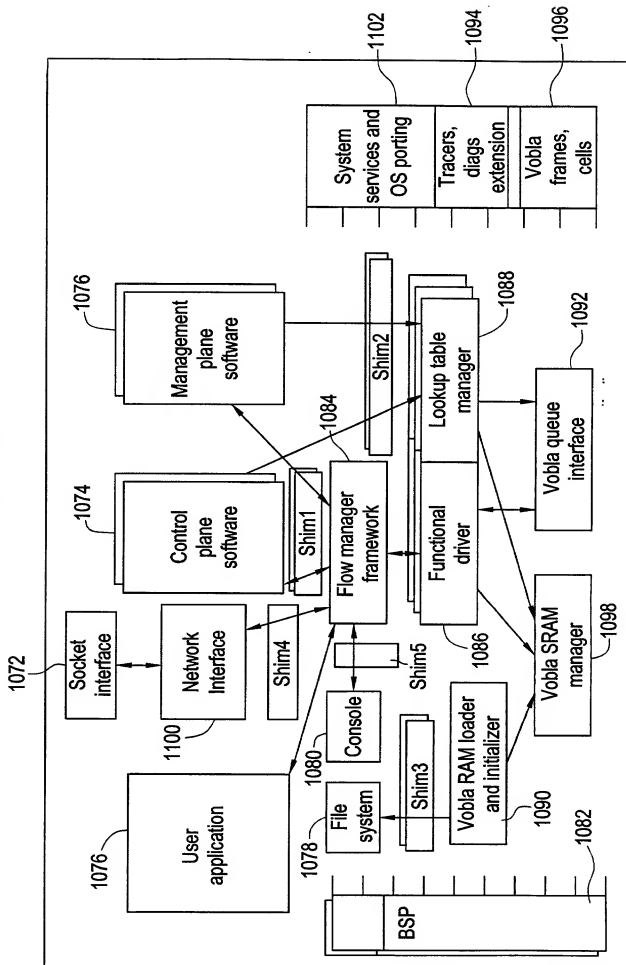


FIG. 86

1200

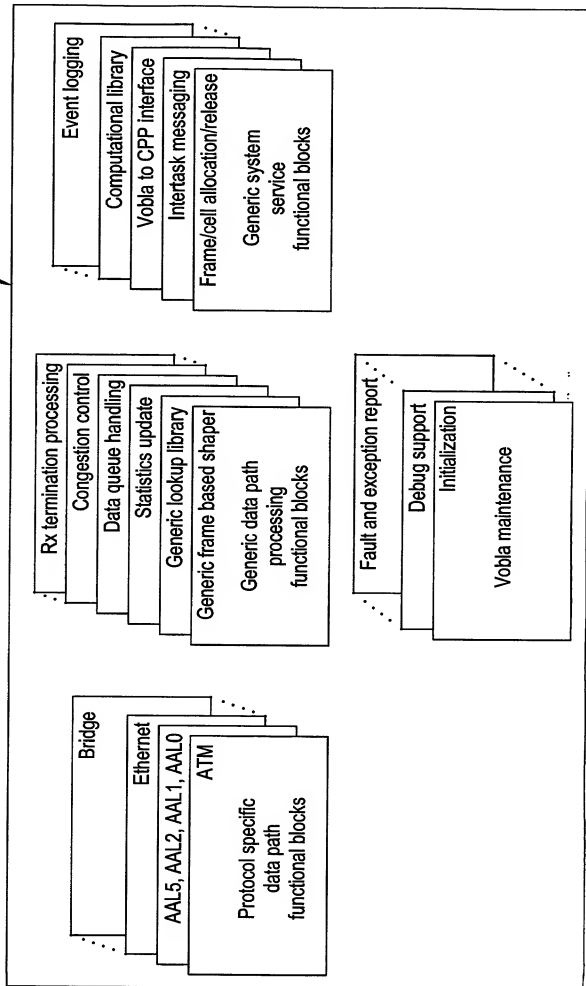




FIG. 87
PRIOR ART

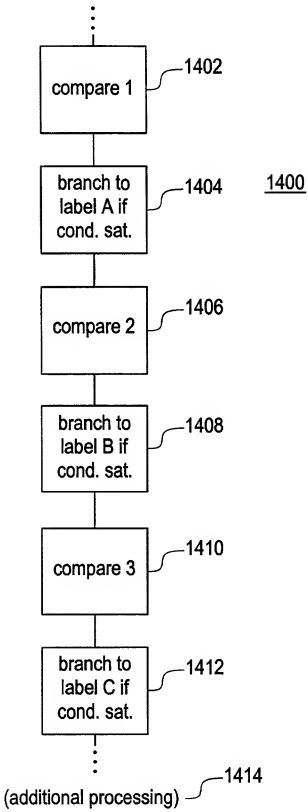


FIG. 88

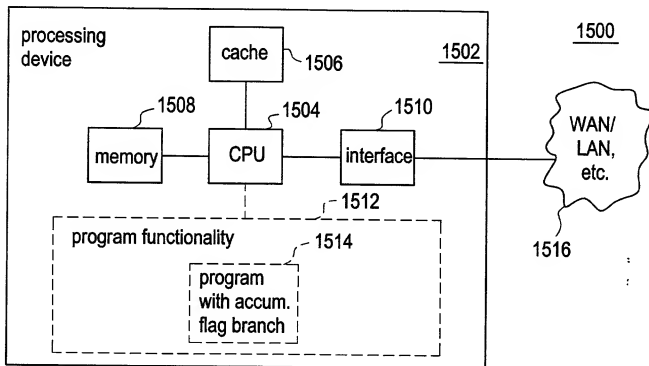


FIG. 89

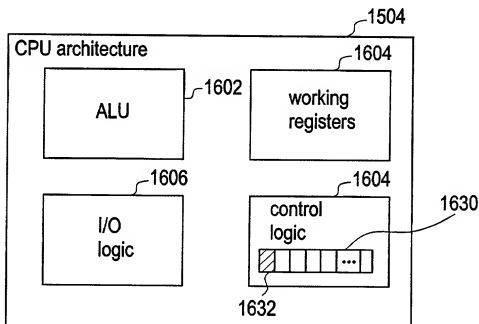


FIG. 90

